FIG. 1

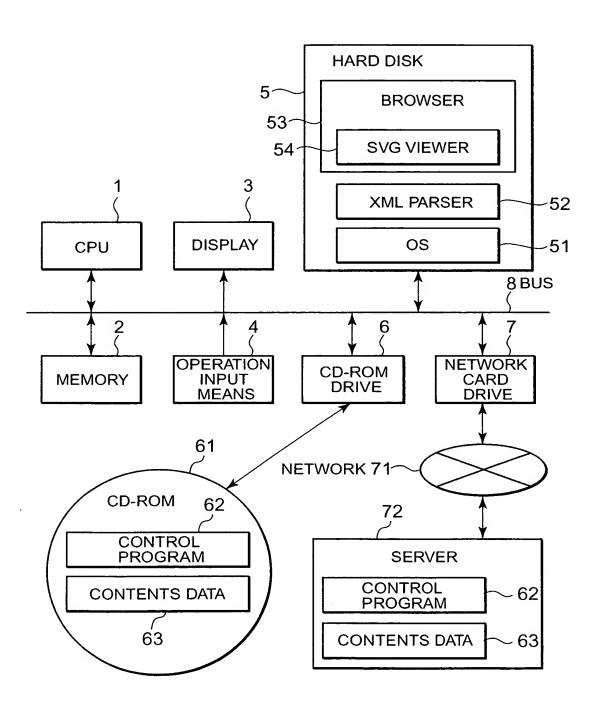


FIG. 2

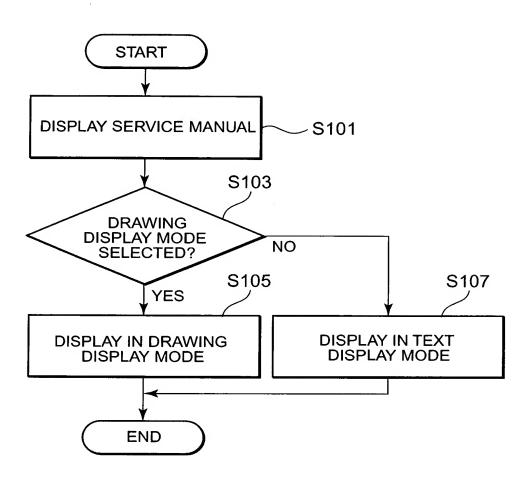


FIG. 3

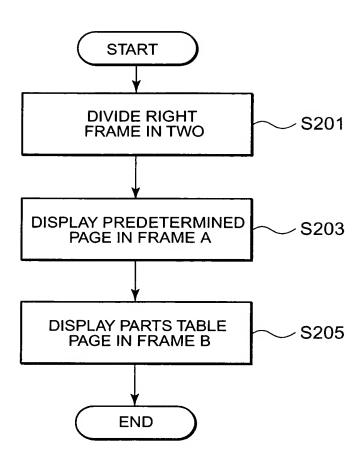
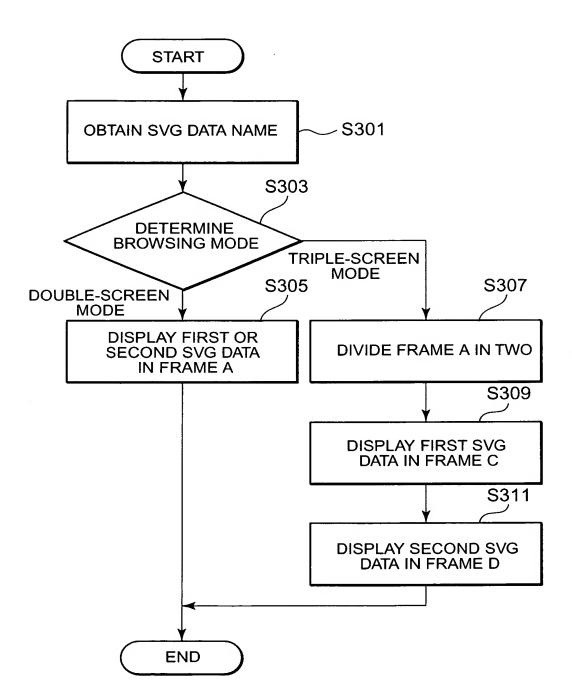


FIG. 4



7,6

FIG. 5

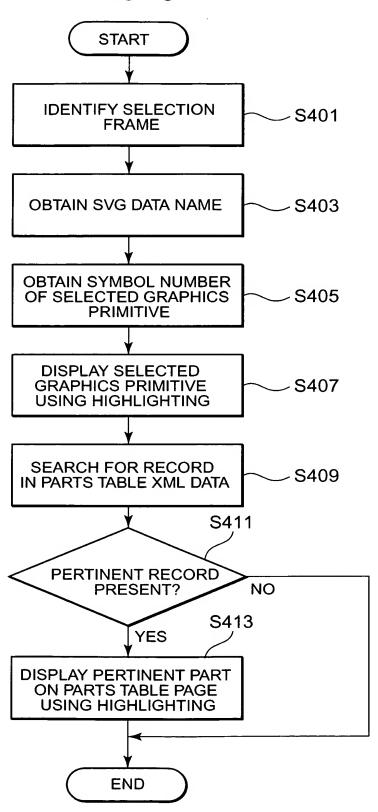


FIG. 6

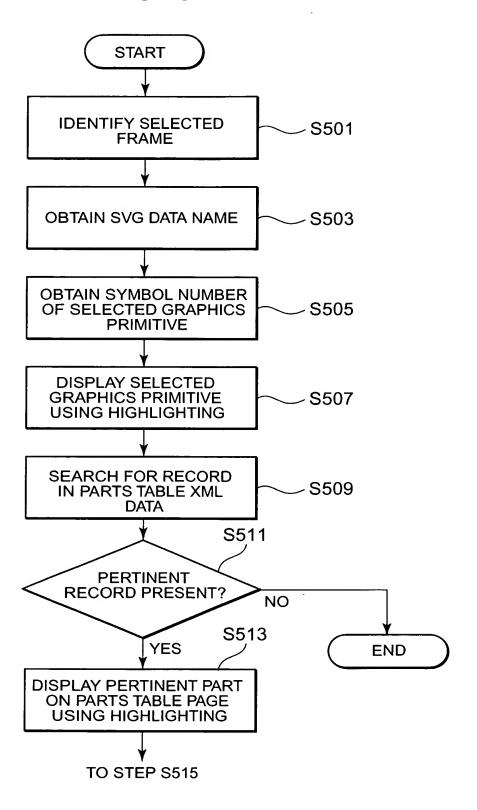


FIG. 7

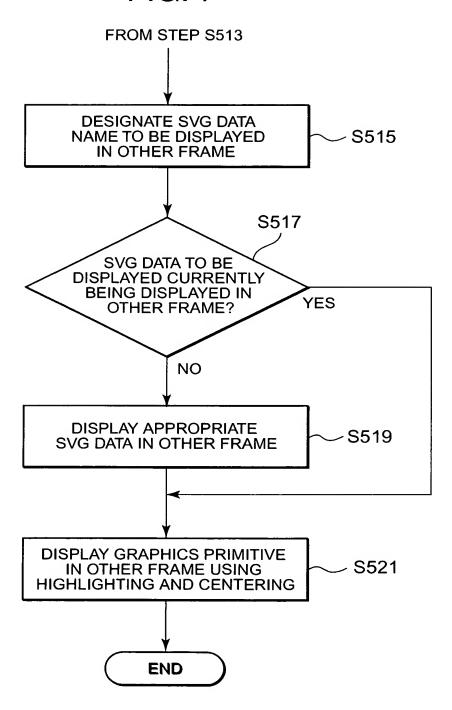
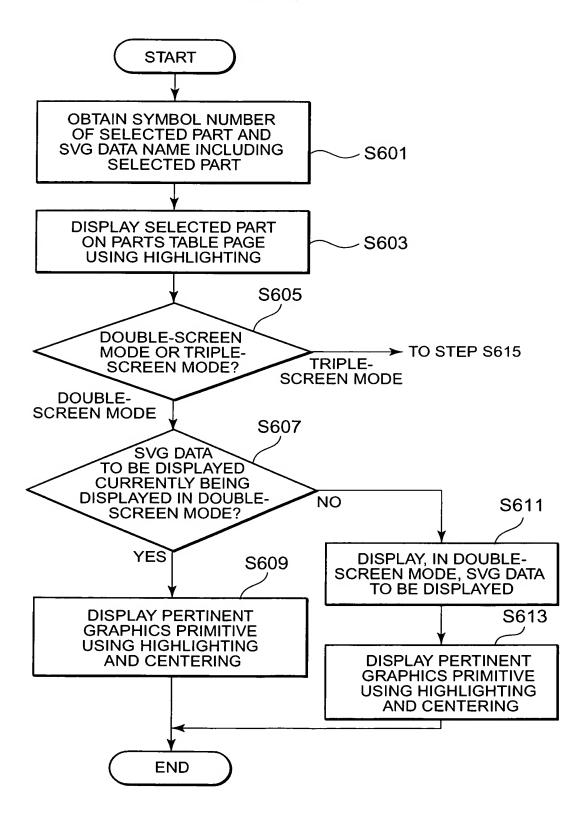


FIG. 8



12.1.2.8.

FIG. 9

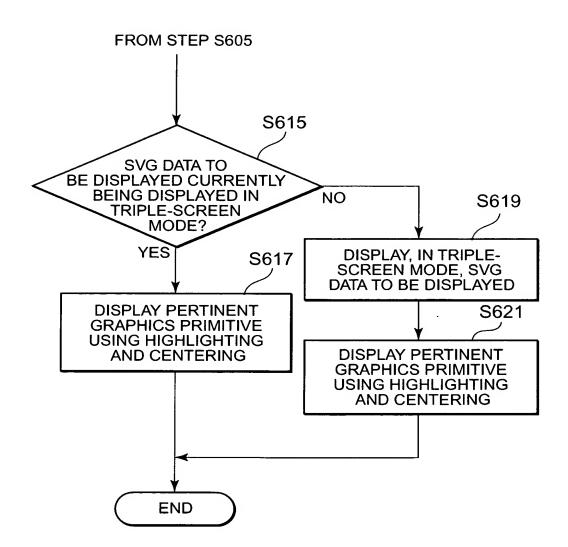
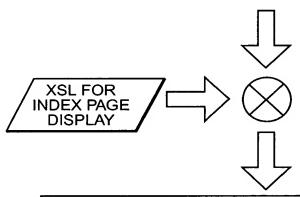


FIG. 10

INDEX XML DATA

CIRCUIT	BROWSING MODE	FIRST SVG DATA NAME	SECOND SVG DATA NAME
SERVO CIRCUIT	1	A1234_s001	A1234_p001
AV DECODER CIRCUIT	1	_A1234_s002	A1234_p001
FLASH ROM CIRCUIT	1	A1234_s003	A1234_p002
VIDEO OUTPUT CIRCUIT	1	A1234_s004	A1234_p003
DIGITAL OUTPUT CIRCUIT	3	-	A1234_p003
SYSTEM CONFIGURATION CIRCUIT	0	A1234_s006	-
\$	\$	\$	\$



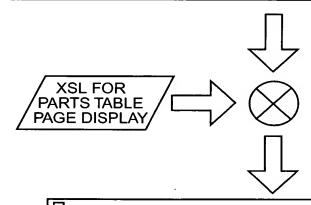
INDEX

- ▲ SERVO CIRCUIT
- ▲ AV DECODER CIRCUIT
- ▲ FLASH ROM CIRCUIT
- ▲ VIDEO OUTPUT CIRCUIT
- ▲ DIGITAL OUTPUT CIRCUIT
- **▲ SYSTEM CONFIGURATION OUTPUT CIRCUIT**

FIG. 11

PARTS TABLE XML DATA

RECORD	SYMBOL NUMBER		PART NAME	~	FIRST SVG DATA NAME	SECOND SVG DATA NAME
1	IC101	AN8703FH-V	IC	~	A1234_s001	A1234_p001
2	IC201	BA5983FM-X	IC	~	A1234_s001	A1234_p001
3	IC251	BA6664FM-X	IC	~	A1234_s001	A1234_p001
4	IC301	MN103S26EGA	IC	~	A1234_s002	A1234_p001
5	IC401	MN102L62GLF3	IC	~	A1234_s002	A1234_p001
6	IC451	S-93C66AFJ-X	0	~	A1234_s003	A1234_p002
7	IC501	NDV8611VWA	IC	· ~	A1234_s003	A1234_p002
\$	\$	\$	5	\$	\$	\$



↓	$ \otimes P \mathcal{D}$		
PARTS TA	BLE		
SYMBOL NUMBER	PART NUMBER	PART NAME	~
IC101	AN8703FH-V	IC	~
IC201	BA5983FM-X	IC	~
IC251	BA6664FM-X	IC	~
IC301	MN103S26EGA	IC	~
IC401	MN102L62GLF3	IC	~
IC451	S-93C66AFJ-X	IC	~
IC501	NDV8611VWA	IC	~
5	\$	\$	\$

FIG. 12

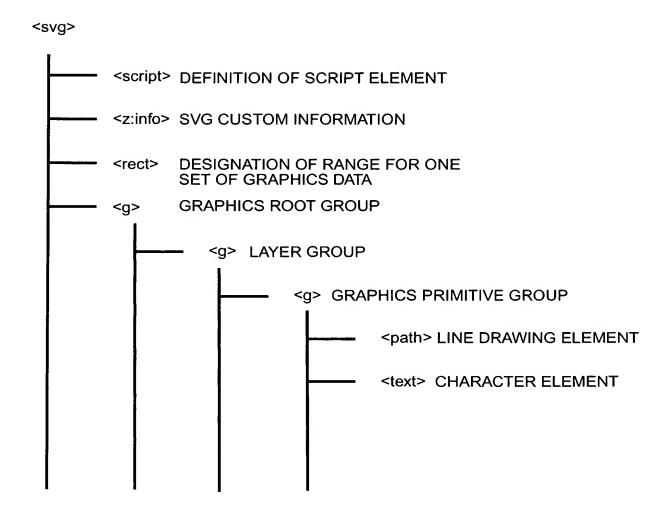


FIG. 13

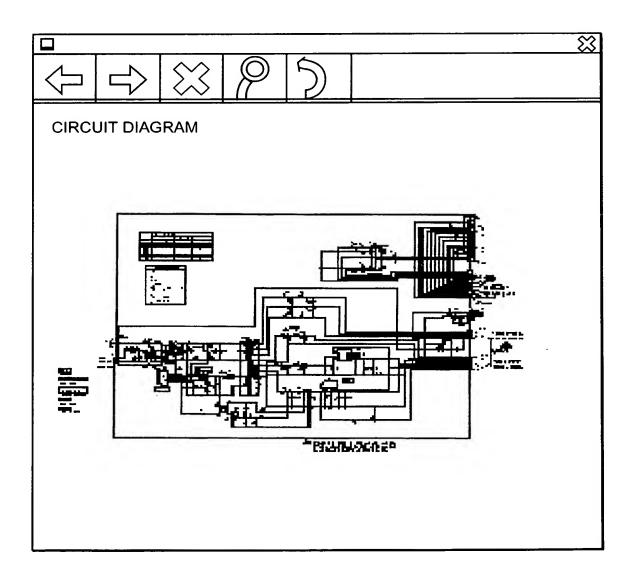


FIG. 14

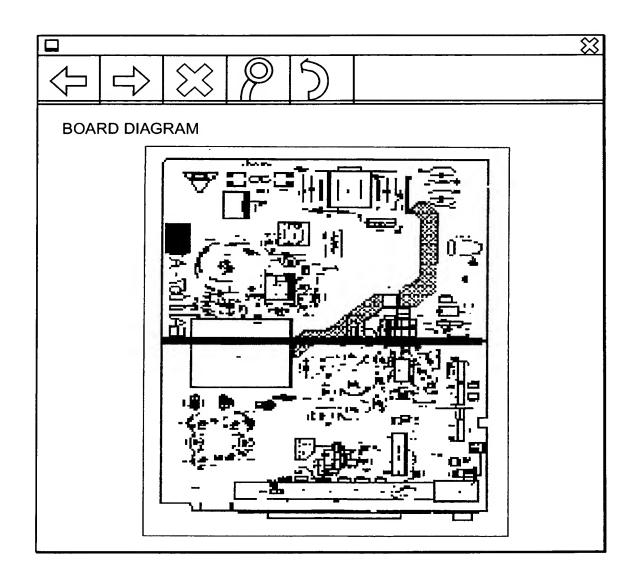


FIG. 15

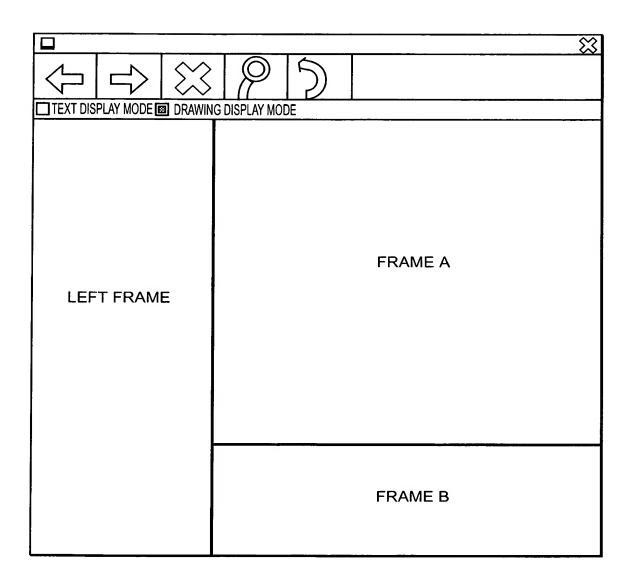
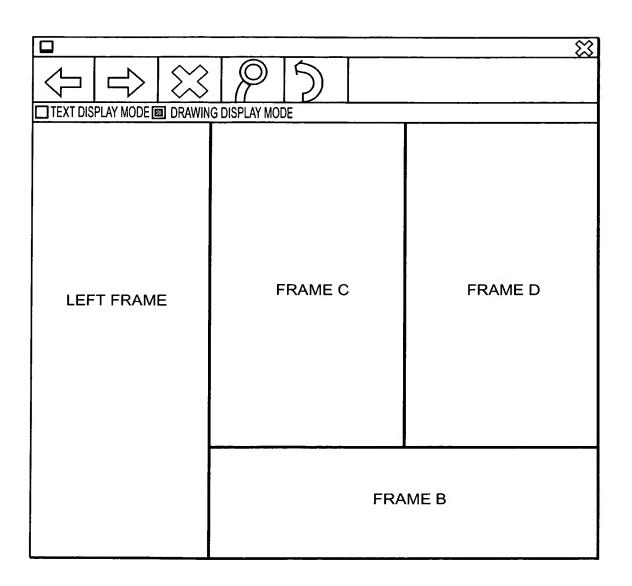


FIG. 16



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FIG. 17

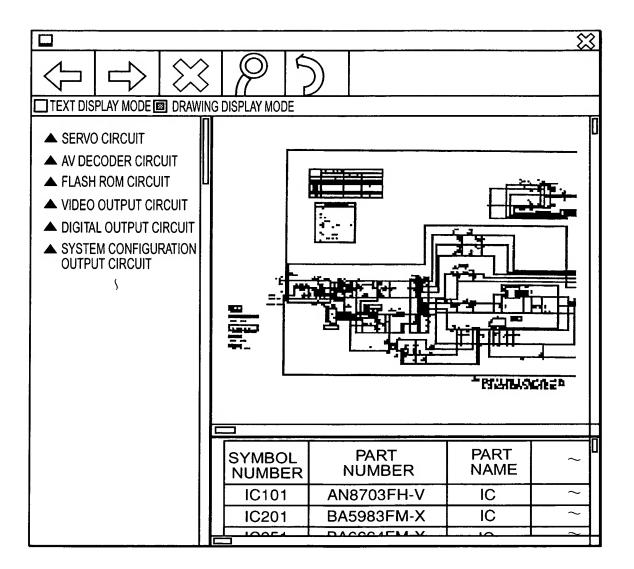
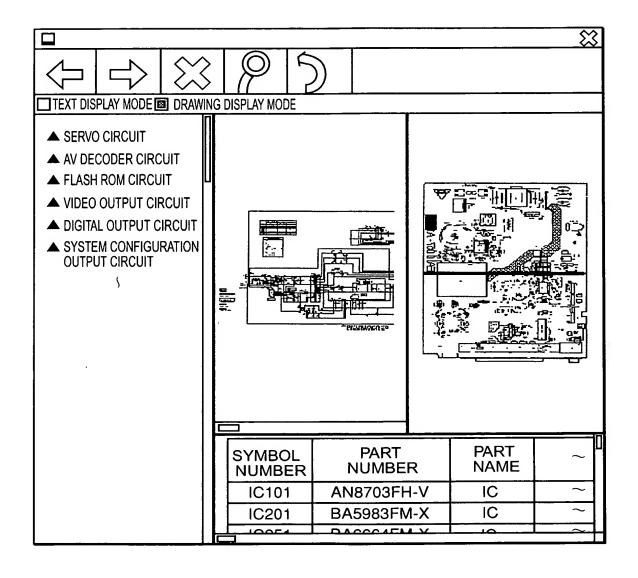


FIG. 18



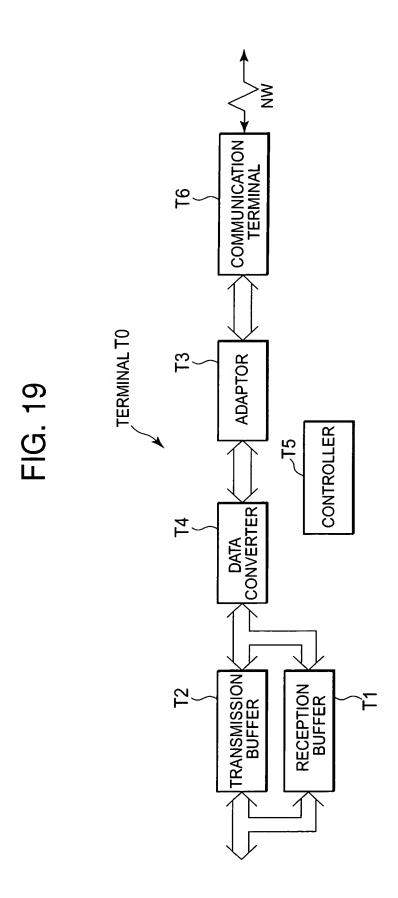
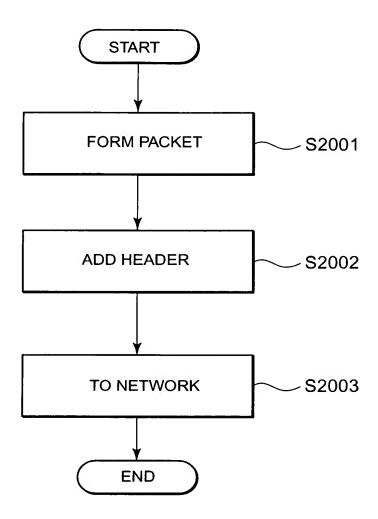


FIG. 20



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FIG. 21

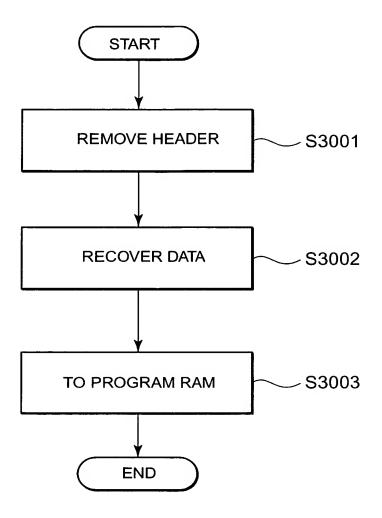
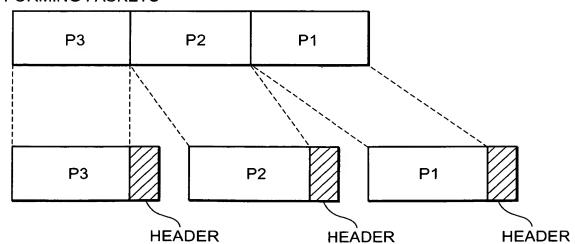


FIG. 22

FORMING PACKETS



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FIG. 23

RECOVERING DATA

